

---

***IN THE UNITED STATES PATENT AND TRADEMARK OFFICE***

---

In re application of: RAMBALDI et al.

Atty Dkt No.: SGSTP009D1/97-B-068D1

Application No.: 10/613,830

Examiner: Hannett, J.

Filed: July 3, 2003

Group: 2622

Title: PIXEL CORRECTION SYSTEM AND  
METHOD FOR CMOS IMAGERS

Confirmation No: 6799

---

CERTIFICATE OF EFS-WEB TRANSMISSION

I hereby certify that this correspondence is being transmitted electronically through EFS-WEB to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450 on April 23, 2007.

Signed: \_\_\_\_\_/Joyce L. Ferreira/  
Joyce L. Ferreira

**CHANGE OF ATTORNEY'S ADDRESS IN APPLICATION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Please send all correspondence for this application as follows:

Lisa Jorgenson  
SGS Thomson Microelectronics, Inc.  
Mail Stop 2346  
1310 Electronics Drive  
Carrollton, TX 75006

Please direct any calls to Lisa Jorgenson at (972) 466-6000

Respectfully submitted,  
BEYER WEAVER LLP

/Godfrey K. Kwan/  
Godfrey K. Kwan  
Registration No. 46,850